

L2000 Programmable Logic Controller

Processor

Multi-Level Password Protection

Assign various functions to appropriate operations
Automatic PLC lock after KeyPress
THREE STRIKES AND OUT password security

Scrolling Text Register Tag Names

Register values are displayed with engineering units
Register names assigned unique twenty character description

Open Architecture without Obsolescence

Examine and Force Any Register Value

Multiple registers can be easily forced to known value

Standard ISA Alarm Sequences are predefined

Choose from any of five unique alarm sequences

Global System -Wide Register Database

Five different counter/timer types
Setpoint registration in write-protected memory
Internal I/O registers
Register counts are configurable up to available memory

2 to 16 Point, Curve Fitting Analog Calibration

Internal digital loop milliamp meter for each AI/AO
Engineering units with floating point accuracy

Industrial Strength Reliability

Five year and ten year warranties available
Complete field service department
Temperature rated to 200° F
Ten year lithium memory battery
Complete battery backup operation, including I/O
Total bumpless "Hot Standby" system available
Watchdog timer
Complete suite of diagnostics built-in
Automatic fault relay form C
Write-protected memory for user program, calibration, and setpoints

TESCO Flowsoft (for L3x)

All analog readouts in engineering units
2U VME parallel bus low power architecture
Multitest module



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For more information on standard features refer to the Processor page.