

L2000 Programmable Logic Controller

Processor Module

Microprocessor

32-bit low power CMOS 68360 25/33 MHz

RAM

Expandable to 16 Mbyte RAM Low Power CMOS
Static RAM dual ported RAM between processors
Battery backed 10 Year battery life selectable write
protected boundary

ROM

Up to 1 Mbyte ROM

Communication Ports

5 Serial ports with full hardware handshake, Baud
Rate Generators Ethernet/IEEE 802.3 10 Mbps with
TCP/IP protocol, full RS-232 and RS-485, 4 serial
DMA channels RISC communications coprocessor.

Real Time Clock

Battery-backed

System

3U VME Parallel bus architecture real time preemptive multitasking operating system with 26
layer kernel

Programming

- IEX 1131-3 Standard Programming Languages
 - Sequential Function Chart (SFC)
 - Structured Text (ST)
 - Instruction List (IL)
 - Function Block Diagram (FBD)
 - Ladder Diagram (LD)
- Tescode™
- ANSI C
- PID Loop Control and All Math Functions Built-In



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For more information on standard features refer to the Features page.